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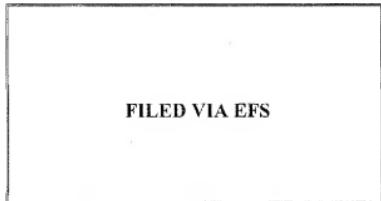
Re: Applicants: Chen et al.
Title: Read and Erase Verify Methods and Circuits Suitable for Low
Voltage Non-Volatile Memories
Application No.: 10/552,948 Filing Date: September 11, 2006
Examiner: Connie C. Yoha Group Art Unit: 2827
Docket No.: SNDK.284US1 Conf. No.: 6753

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) This Transmittal Letter; and
- (2) Power of Attorney by Assignee of Entire Interest (2 pages).

No additional fee is required.



Respectfully submitted,

Michael G. Cleveland
Reg. No. 46,030